PALM INTRANET

Day : Monday Date: 10/8/2007

Time: 15:21:58

Inventor Name Search Result

Your Search was:

Last Name = ARDHANARI First Name = GURUPRASAD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10526529	Not Issued	30			ARDHANARI, GURUPRASAD
10526700	Not Issued	30			ARDHANARI, GURUPRASAD
10526886	Not Issued	30		Method and system for controlling bandwidth allocation	ARDHANARI, GURUPRASAD
11364740	Not Issued	25	r'l	Data switch, and communication system using the data switch	ARDHANARI, GURUPRASAD
11674501	Not Issued	20		METHOD OF CONTROLLING A DATA FLOW, TRANSMITTER AND DATA TRANSMISSION SYSTEM	ARDHANARI, GURUPRASAD

Inventor Search Completed: No Records to Display.

Search Another: Inventor Last Name First Name
Ardhanari Guruprasad Search

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Day : Monday Date: 10/8/2007

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Inventor Name Search Result

Your Search was:

Last Name = MISHRA First Name = SHRIDHAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09964204	Not Issued	==		Testing of SOC	MISHRA, SHRIDHAR MUBARAQ
10065005	7080184	150		ISDN-BASED BUS INTERFACE	MISHRA, SHRIDHAR MUBARAQ
10526529	Not Issued	30	03/02/2005	Data switch and a method for broadcast packet queue estimation	MISHRA, SHRIDHAR MUBARAQ
10526700	Not Issued	30	03/04/2005	Method and system for determining conformance of a data key with rules by means of memory lookups	MISHRA, SHRIDHAR MUBARAQ
10526811	Not Issued	30	03/04/2005	Intelligent stacked switching system	MISHRA, SHRIDHAR MUBARAQ
10526886	Not Issued	30	03/04/2005	Method and system for controlling bandwidth allocation	MISHRA, SHRIDHAR MUBARAQ
10526992	Not Issued	30	03/07/2005	Parser for parsing data packets	MISHRA, SHRIDHAR MUBARAQ
10526990	Not Issued	30	03/07/2005	Configurable fast ethernet and gigabit ethernet data port	MISHRA, SHRIDHAR MUBARAQ

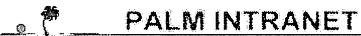
Inventor Search Completed: No Records to Display.

Const. Associations Toronto.	Last Name	First Name	
Search Another: Inventor	Mishra	Shridhar	Search

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Day : Monday Date: 10/8/2007

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Inventor Name Search Result

Your Search was:

Last Name = PANDEY First Name = PRAMOD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10094069	Not Issued	161	03/08/2002	Integrated circuit with improved clock distribution	PANDEY, PRAMOD
10363189	6934205	150	07/28/2003	BIST FOR PARALLEL TESTING OF ON CHIP MEMORY	PANDEY, PRAMOD
10523517	Not Issued	80	02/04/2005	Methods for operating a CPU having an internal data cache	PANDEY, PRAMOD
10817248	7217794	150	04/02/2004	COMPOUNDS AND METHODS FOR TREATMENT OF THROMBOSIS	PANDEY, PRAMOD
60459910	Not Issued	159	04/02/2003	Three-dimensional structures of factor XIa, inhibitors of factor XIa, and uses thereof	PANDEY, PRAMOD
10592322	Not Issued	19	01/01/0001	Novel protein layering masses, processes for the manufacture thereof, and related products	PANDEY, PRAMOD K.
09856192	6495925	150	01/04/2002	SEMICONDUCTOR CHIP AND A LEAD FRAME	PANDEY, PRAMOD KUMAR
10526811	Not Issued	30	03/04/2005	Intelligent stacked switching system	PANDEY, PRAMOD KUMAR
10526886	Not Issued	30	03/04/2005	Method and system for controlling bandwidth allocation	PANDEY, PRAMOD KUMAR
11488819	Not Issued	20	07/19/2006	High protein aerated food composition	PANDEY, PRAMOD KUMAR
11522979	Not Issued	25	09/19/2006	Non sweet binder for savory food product	PANDEY, PRAMOD KUMAR
60552437	· Not Issued	159		Novel high protein confectionery layering materials, processes for the manufacture thereof, and nutritional products containing these layering materials	PANDEY, PRAMOD KUMAR
60700326	Not	159	07/19/2005	High protein aerated food	PANDEY,



	Issued		1 1	composition and method of preparation thereof	PRAMOD KUMAR
60717760	Not Issued	159	1		PANDEY, PRAMOD KUMAR

Inventor Search Completed: No Records to Display.

Soonah Anothon: Inventor	Last Name	First Name	
Search Another: Inventor	Pandey	Pramod	Search

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